



PTO/SB/08A (08-03)

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Sheet	1	of	3
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Application Number	09/650,851
Filing Date	08/29/2000
First Named Inventor	Mandeep Singh Chadha
Art Unit	2634
Examiner Name	Shuwang Liu
Attorney Docket Number	M. CHADHA 1-1-1-1

U. S. PATENT DOCUMENTS

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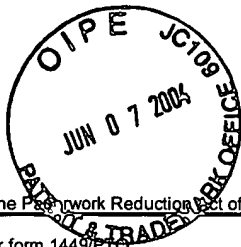
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Examiner Name	Shuwang Liu
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Floyd M. Gardner "Interpolation in Digital Modems - Part I: Fundamentals" IEEE Transactoins on Communications, Vol. 41, No. 3, March 1993; pp. 501-507	
		Lars Erup, et al. "Interpolation in Digital Modems - Part II: Implementation and Performance" IEEE Transactions on Communications, Vol. 41, No. 6, June 1993; pp. 998-1008	
		James. D. Barnette, et al.; U.S. Patent Application Serial No. 09/652,116, filed 08/29/2000 entitled "Resampler for a bit Pump and Method of Resampling a Signal Associated Therewith"; pp. 1-59; 6 drawing sheets	
		Mandeep Singh Chadha, et al.; U.S. Patent Application Serial No. 09/650,854, filed 08/29/2000 entitled "Separation Circuit for an Echo Canceling System and Method of Operating the Same"; pp. 1-67; 6 drawing sheets	
		Mandeep Singh Chadha, et al.; U.S. Patent Application Serial No. 09/650,853, filed 08/29/2000 entitled "Echo Canceling System for a Bit Pump and Method of Opearting the Same"; pp. 1-60; 5 drawing sheets	
		James D. Barnette; U.S. Patent Application Serial No. 09/650,850, filed 08/29/2000 entitled "An Interpolator, a Resampler Employing the Interpolator and Method of Interpolating a Signal Associated Therewith"; pp. 1-78; 6 drawing sheets	

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